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APPLICATION NO.	FI	ILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/007,058	10/007,058 12/04/2001		Jeong-Dae Son	678-714(P9741)	6217
28249	7590	06/07/2005		EXAMINER	
DILWORT	H & BAI	RRESE, LLP	NGUYEN, KHAI MINH		
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UNIONDAI	LE, NY 1	11553		ART UNIT	PAPER NUMBER
			•	2687	

DATE MAILED: 06/07/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

		Appli	ication No.	Applicant(s)				
Office Action Summary			07,058	SON, JEONG-DAE				
			niner	Art Unit				
		Khai	M Nguyen	2687				
The MAILING DATE of this communication appears on the cover sheet with the correspondence address								
Period for Reply A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely. - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication. - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).								
Status								
1)⊠ Re	Responsive to communication(s) filed on 27 December 2004.							
2a) <u></u> ⊤h	is action is FINAL.	2b)⊠ This actior	is non-final.					
, —	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.							
Disposition of Claims								
 4) Claim(s) 1-12 is/are pending in the application. 4a) Of the above claim(s) is/are withdrawn from consideration. 5) Claim(s) is/are allowed. 6) Claim(s) 1-12 is/are rejected. 7) Claim(s) is/are objected to. 8) Claim(s) are subject to restriction and/or election requirement. 								
Application	Papers							
10)∐ The Ap Re	e specification is objected to by the drawing(s) filed on is/are oplicant may not request that any objected to all or declaration is objected to	a) accepted ction to the drawing the correction is r	g(s) be held in abeyance. Se equired if the drawing(s) is ob	e 37 CFR 1.85(a). pjected to. See 37 CFR 1.121(d).				
Priority und	der 35 U.S.C. § 119							
 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 								
2) Notice o	of References Cited (PTO-892) If Draftsperson's Patent Drawing Review (I Ition Disclosure Statement(s) (PTO-1449 o O(s)/Mail Date		4) Interview Summar Paper No(s)/Mail D 5) Notice of Informal 6) Other:					

DETAILED ACTION

Response to Amendment

This Office Action is response to Amendment filed on 12/27/2004.
 Claims 1-12 are pending.

Response to Arguments

2. Applicant's arguments with respect to claims 1-12 have been considered but are moot in view of the new ground(s) of rejection.

Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 1-12 are rejected under 35 U.S.C. 102(e) as being anticipated by Delchini (U.S.Pub-2001002956).

Regarding claim 1, Delchini teaches method of sharing a program using target board identifications (IDs) in a mobile communication system (paragraph 0009), comprising the steps of:

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executing by a main processor a shared execution file for a plurality of target boards in the system when power is supplied to the system (fig.1-4, abstract, paragraph 0030-0034), said main processor apart from said plurality of target boards (fig.1-4, paragraph 0009);

reading by a main processor a target board ID of each target board (fig.1-4, paragraph 0051-0053, 0059); initializing target board hardware according to the target board ID (paragraph 0009, 0051-0053, 0059);

initializing by a main processor an operating system (OS) for each target board using the target board ID (fig.3, paragraph 0059-0066); and

branching by a main processor into a sub-routine for each target board according to the target board ID and executing by a main processor an application program for the target board (fig.1-4, paragraph 0009, 0023-0028, 0074).

Regarding claim 2, Delchini teaches the method of claim 1, wherein the OS initialization varies according to the hardware structure and running conditions of each target board (fig.1-4, paragraph 0009, 0031-0033).

Regarding claim 3, Delchini teaches the method of claim 1, wherein an application program for each target board according to the function of the target board is

stored as part of a single master application program and executed in the application execution step (fig.1-4, abstract, paragraph 0031-0034, 0064-0067).

Regarding claim 4, Delchini teaches the method of claim 1, wherein common functions of the target boards are incorporated into the shared execution file (fig.3-4, paragraph 0059-0064).

Regarding claim 5, Delchini teaches the method of claim 1, wherein different functions of the target boards are implemented in branch sub-routines according to the target board lds (fig.1-4, paragraph 0009, 0023-0028, 0074).

Regarding claim 6, Delchini teaches a method of sharing a program in a mobile communication system (fig.1-4, paragraph 0009), comprising the steps of:

storing by a main processor a master execution file in a memory (paragraph 0023-026), said master execution file compiled to contain program code to operate a plurality of target boards of the mobile communication system (fig.1-4, abstract, paragraph 0030-0034), said main processor apart from said plurality of target boards (paragraph 0009);

executing by a main processor the master execution file for the plurality of target boards in the system when power is supplied to the system (fig.1-4, abstract, paragraph 0030-0034);

reading by a main processor a target board identification (ID) of each target board (fig.1-4, paragraph 0051-0053, 0059); initializing target board hardware according to the target board ID and the master execution file (paragraph 0009, 0051-0053, 0059);

initializing by a main processor an operating system (OS) for each target board using the target board ID and master execution file (fig.3, paragraph 0059-0066); and

branching by a main processor into a sub-routine for each target board according to the target board ID and executing by a main processor an application program of the master execution file for each target board (fig.3, paragraph 0059-0066).

Regarding claim 7, Delchini teaches the method of claim 6, wherein the OS initialization varies according to the hardware structure and running conditions of each target board (fig.1-4, paragraph 0009, 0031-0033).

Regarding claim 8, Delchini teaches the method of claim 6, wherein an application program is compiled into the master execution file for each target board according to the function of the target board and executed in the application execution step (fig.1-4, abstract, paragraph 0031-0034, 0064-0067).

Regarding claim 9, Delchini teaches the method of claim 6, wherein common functions of the target boards are incorporated into the master execution file (fig.3-4, paragraph 0059-0064).

Regarding claim 10, Delchini teaches the method of claim 6, wherein different functions of the target boards are implemented according to the target board IDs in branch sub-routines of the master execution file (fig.1-4, paragraph 0009, 0023-0028, 0074.

Regarding claim 11, Delchini teaches a system for sharing a program in a mobile communications system having a control unit and a plurality of target boards (fig.1-4, paragraph 0009, 0053, 0056-0061), comprising: wherein the control unit stores an execution file compiled to contain program code to operate each of the plurality of target boards (paragraph 0049, 0056-0061).

Regarding claim 12, Delchini teaches the system for sharing a program in a mobile communications system of claim 11, wherein each of the plurality of target boards is assigned an identification (ID) code that the control unit reads and executes

subroutines of the execution file according to the ID code of the target board (abstract, paragraph 0009, 0025-0032).

Citation of Pertinent Prior Art

4. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Huan et al. (U.S.Pat-20040153526) discloses Providing a reliable operating system for clients of a net-booted environment.

Beaudou (U.S.Pat-6671522) discloses thermal controlled by a subscriber's identification module for running an application.

Conclusion

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Khai M Nguyen whose telephone number is 571.272.7923. The examiner can normally be reached on 8:00-5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Lester Kincaid can be reached on 571.272.7922. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Khai Nguyen Au:2687

5/17/2004

ELISEO RAMOS-FELICIANO PATENT EXAMINER